## General Description

The AAT4610B SmartSwitch is a current limited P-channel MOSFET power switch designed for high-side load switching applications. This switch operates with inputs ranging from 2.4 V to 5.5 V , making it ideal for both 3 V and 5 V systems. An integrated current-limiting circuit protects the input supply against large currents which may cause the supply to fall out of regulation. The AAT4610B is also protected from thermal overload which limits power dissipation and junction temperatures. It can be used to control loads that require up to 1 A . Current limit threshold is programmed with a resistor from SET to ground. The quiescent supply current is typically a low $9 \mu \mathrm{~A}$. In shutdown mode, the supply current decreases to less than $1 \mu \mathrm{~A}$.

The AAT4610B is available in a Pb-free 5 -pin SOT23 or 8 -pin SC70JW package and is specified over the $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ temperature range.

## Features

- Input Voltage Range: 2.4 V to 5.5 V
- Programmable Over-Current Threshold
- Fast Transient Response:
- 400ns Response to Short Circuit
- Low Quiescent Current
- $9 \mu \mathrm{~A}$ Typical
- $1 \mu \mathrm{~A}$ Max with Switch Off
- $145 \mathrm{~m} \Omega$ Typical $\mathrm{R}_{\mathrm{DS}(\mathrm{on})}$
- Only 2.5V Needed for ON/OFF Control
- Under-Voltage Lockout
- Thermal Shutdown
- 4 kV ESD Rating
- UL Approved-File No. E217765
- 5-Pin SOT23 or 8-Pin SC70JW Package
- Temperature Range: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$


## Applications

- Hot Swap Supplies
- Notebook Computers
- Peripheral Ports
- Personal Communication Devices


UL Recognized Component

## Typical Application



## Pin Descriptions

| Pin Number |  |  |  |
| :---: | :---: | :--- | :--- |
| SOT23-5 | SC70JW-8 |  | Function |
| 1 | 5 | OUT | P-channel MOSFET drain. Connect a $0.47 \mu$ F capacitor from OUT to GND. |
| 2 | $6,7,8$ | GND | Ground connection. |
| 3 | 1 | SET | Current limit set input. A resistor from SET to ground sets the current limit for <br> the switch. |
| 4 | 2 | $\overline{\text { ON }}$ | Enable input. Two versions are available, active-high and active-low. See Order- <br> ing Information for details. |
| 5 | 3,4 | IN | P-channel MOSFET source. Connect a $1 \mu \mathrm{~F}$ capacitor from IN to GND. |

## Pin Configuration

SOT23-5
(Top View)


SC70JW-8
(Top View)


## Absolute Maximum Ratings ${ }^{1}$

$T_{A}=25^{\circ} \mathrm{C}$, unless otherwise noted.

| Symbol | Description | Value | Units |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\text {IN }}$ | IN to GND | -0.3 to 6 | V |
| $\mathrm{~V}_{\text {ON }}$ | $\overline{\text { ON }}($ ON) to GND | -0.3 to $\mathrm{V}_{\text {IN }}+0.3$ | V |
| $\mathrm{~V}_{\text {SET, }} \mathrm{V}_{\text {OUT }}$ | SET, OUT to GND | -0.3 to $\mathrm{V}_{\text {IN }}+0.3$ | V |
| $\mathrm{I}_{\text {MAX }}$ | Maximum Continuous Switch Current | 2 | A |
| $\mathrm{~T}_{3}$ | Operating Junction Temperature Range | -40 to 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {LEAD }}$ | Maximum Soldering Temperature (at Leads) | 300 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{V}_{\text {ESD }}$ | ESD Rating ${ }^{\circ}-\mathrm{HBM}$ | 4000 | V |

## Thermal Characteristics ${ }^{3}$

| Symbol | Description | Value | Units |
| :---: | :--- | :---: | :---: |
| $\Theta_{J A}$ | Thermal Resistance (SOT23-5 or SC70JW-8) | 150 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{P}_{\mathrm{D}}$ | Power Dissipation (SOT23-5 or SC70JW-8) | 667 | mW |

[^0]
## Electrical Characteristics

$\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

| Symbol | Description | Conditions |  | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IN }}$ | Operation Voltage |  |  | 2.4 |  | 5.5 | V |
| $\mathrm{I}_{\mathrm{Q}}$ | Quiescent Current | $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}, \mathrm{ON}(\overline{\mathrm{ON}})=$ Active, $\mathrm{I}_{\text {OUT }}=0$ |  |  | 9 | 25 | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {Q(OFF) }}$ | Off Supply Current | $\mathrm{ON}(\overline{\mathrm{ON}})=$ Inactive, $\mathrm{V}_{\text {IN }}=5.5 \mathrm{~V}$ |  |  |  | 1 | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {SD(OFF) }}$ | Off Switch Current | $\mathrm{ON}(\overline{\mathrm{ON}})=$ Inactive, $\mathrm{V}_{\text {IN }}=5.5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=0$ |  |  | 0.01 | 1 | $\mu \mathrm{A}$ |
| $\mathrm{V}_{\text {UvLo }}$ | Under-Voltage Lockout | Rising Edge, 1\% Hysteresis |  |  | 1.8 | 2.4 | V |
| $\mathrm{R}_{\mathrm{DS} \text { (ON) }}$ | On Resistance | $\mathrm{V}_{\mathrm{IN}}=5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | 145 | 180 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\mathrm{IN}}=4.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | 150 |  |  |
|  |  | $\mathrm{V}_{\mathrm{IN}}=3.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | 190 | 230 |  |
| TC RDS | On Resistance Temperature Coefficient |  |  |  | 2800 |  | ppm $/{ }^{\circ} \mathrm{C}$ |
| $\mathrm{I}_{\text {LIM }}$ | Current Limit | $\mathrm{R}_{\text {SET }}=6.8 \mathrm{k} \Omega$ |  | 1.2 | 1.6 | 2.0 | A |
| $\mathrm{I}_{\text {Lim(Min) }}$ | Minimum Current Limit |  |  |  | 130 |  | mA |
| $\mathrm{V}_{\text {on(L) }}$ | ON ( $\overline{\mathrm{ON}}$ ) Input Low Voltage | $\mathrm{V}_{\mathrm{IN}}=2.7 \mathrm{~V}$ to $5.5 \mathrm{~V}^{1}$ |  |  |  | 0.8 | V |
| $\mathrm{V}_{\text {ON(H) }}$ | ON ( $\overline{\mathrm{ON}}$ ) Input High Voltage | $\mathrm{V}_{\mathrm{IN}}=2.7 \mathrm{~V}$ to $<4.2 \mathrm{~V}^{1}$ |  | 2.0 |  |  | V |
|  |  | $\mathrm{V}_{\text {IN }} \geq 4.2 \mathrm{~V}$ to $5.0 \mathrm{~V}^{1}$ |  | 2.4 |  |  |  |
| $\mathrm{I}_{\text {on(SINK) }}$ | ON ( $\overline{\mathrm{ON}}$ ) Input Leakage | $\mathrm{V}_{\text {ON }}=5.5 \mathrm{~V}$ |  |  | 0.01 | 1 | $\mu \mathrm{A}$ |
| $\mathrm{T}_{\text {RESP }}$ | Current Limit Response Time | $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}$ |  |  | 0.4 |  | $\mu \mathrm{s}$ |
| Toff | Turn-Off Time | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=10 \Omega$ |  |  | 4 | 12 | $\mu \mathrm{s}$ |
| $\mathrm{T}_{\text {on }}$ | Turn-On Time | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=10 \Omega$ |  |  | 12 | 200 | $\mu \mathrm{s}$ |
| $\mathrm{T}_{\text {SD }}$ | Over-Temperature Threshold | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}$ | $\mathrm{T}_{\mathrm{J}}$ Increasing |  | 125 |  | ${ }^{\circ} \mathrm{C}$ |
|  |  |  | T, Decreasing |  | 115 |  |  |

[^1]
## Typical Characteristics

Unless otherwise noted, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

## Quiescent Current vs. Temperature



Output Current vs. Output Voltage $\left(R_{\text {SET }}=22.1 \mathrm{k} \Omega\right)$


Off-Supply Current vs. Temperature


Quiescent Current vs. Input Voltage

$\mathrm{R}_{\mathrm{DS}(\mathrm{ON})} \mathrm{vs}$. Temperature


## Off-Switch Current vs. Temperature



## Typical Characteristics

Unless otherwise noted, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

Turn-On vs. Temperature
$\left(R_{\text {LOAD }}=10 \Omega ; C_{\text {LOAD }}=0.47 \mu \mathrm{~F}\right)$


Turn-On
$\left(R_{L}=10 \Omega ; C_{L}=0.47 \mu F\right)$


Turn-Off vs. Temperature
$\left(R_{\text {LOAD }}=10 \Omega ; C_{\text {LOAD }}=0.47 \mu \mathrm{~F}\right)$


Turn-Off
$\left(R_{L}=10 \Omega ; C_{L}=0.47 \mu F\right)$

$V_{\text {IH }}$ and $V_{\text {IL }}$ vs. $V_{\text {IN }}$


## Typical Characteristics

Unless otherwise noted, $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.
$\mathrm{R}_{\text {SET }}$ vs. $\mathbf{I}_{\text {LIM }}$

$\mathbf{R}_{\text {SET }}$ Coefficient vs. I LIM


Output Current vs. Temperature

$$
\left(\mathrm{R}_{\mathrm{SET}}=22.1 \mathrm{k} \Omega\right)
$$



## Functional Block Diagram



## Application Information

## Setting Current Limit

In most applications, the variation in $\mathrm{I}_{\text {LIM }}$ must be taken into account when determining $R_{\text {SEt. }}$. The $I_{\text {LIm }}$ variation is due to processing variations from part to part, as well as variations in the voltages at IN and OUT, plus the operating temperature. See charts "Current Limit vs. Temperature" and "Output Current vs. Vout." Together, these three factors add up to a $\pm 25 \%$ tolerance (see $\mathrm{I}_{\text {LIM }}$ specification in Electrical Characteristics section). Figure 1 illustrates a cold device with a statistically higher current limit and a hot device with a statistically lower current limit, both with $R_{\text {SET }}$ equal to $8.87 \mathrm{k} \Omega$. While the chart, "R $\mathrm{R}_{\text {SET }}$ Vs. $\mathrm{I}_{\text {LIM }}$ " indicates an $\mathrm{I}_{\text {LIM }}$ of 1.1 A with an $\mathrm{R}_{\text {SET }}$ of $10.5 \mathrm{k} \Omega$, this figure shows that the actual current limit will be at least 0.825 A and no greater than 1.375A.

To determine $\mathrm{R}_{\mathrm{Set}}$ start with the maximum current drawn by the load and multiply it by 1.33 (typical $\mathrm{I}_{\text {LIM }}=$ minimum $\mathrm{I}_{\text {Lim }} / 0.75$ ). This is the typical current limit value. Next, refer to "R $\mathrm{R}_{\text {SET }}$ vs. $\mathrm{I}_{\text {LIM }}$ " and find the $\mathrm{R}_{\text {SET }}$ that corresponds to the typical current limit value. Choose the
largest resistor available that is less than or equal to it. For greater precision, the value of $R_{\text {SET }}$ may also be calculated using the $I_{\text {Lim }} R_{\text {SET }}$ product found in the chart " $R_{\text {SET }}$ Coefficient vs. $\mathrm{I}_{\text {Lim." }}$ The maximum current is derived by multiplying the typical current for the chosen $\mathrm{R}_{\text {SET }}$ in the chart by 1.25. A few standard resistor values are listed in Table 1.

Current Limit vs. Output Voltage
( $\mathrm{R}_{\text {SET }}=8.87 \mathrm{k} \Omega$ )


Figure 1: Current Limit Using 10.5k $\mathbf{\Omega}$.

| $\mathbf{R}_{\mathbf{s e t}}$ <br> $(\mathbf{k} \boldsymbol{\Omega})$ | Current <br> Limit Typ <br> $(\mathbf{m A})$ | Device Will <br> Not Current <br> Limit Below <br> $(\mathbf{m A})$ | Device Always <br> Current Limits <br> Below (mA) |
| :---: | :---: | :---: | :---: |
| 40.2 | 225 | 169 | 281 |
| 30.9 | 300 | 225 | 375 |
| 24.9 | 350 | 263 | 438 |
| 22.1 | 425 | 319 | 531 |
| 19.6 | 450 | 338 | 563 |
| 17.8 | 525 | 394 | 656 |
| 16.2 | 600 | 450 | 750 |
| 14.7 | 675 | 506 | 844 |
| 13 | 700 | 525 | 875 |
| 10.5 | 1000 | 750 | 1250 |
| 8.87 | 1100 | 825 | 1375 |
| 7.5 | 1325 | 994 | 1656 |
| 6.81 | 1600 | 1200 | 2000 |
| 6.04 | 1925 | 1444 | 2406 |
| 5.49 | 1950 | 1463 | 2438 |
| 4.99 | 2100 | 1575 | 2625 |
| 4.64 | 2350 | 1763 | 2938 |

Table 1: Current Limit $\mathbf{R}_{\text {SET }}$ Values

Example: A USB port requires 0.5A. 0.5A multiplied by 1.33 is $0.665 A$. From the chart named "R $R_{\text {SEt }}$ vs. $I_{\text {LIM, }}$ " $R_{\text {SEt }}$ should be less than $18 \mathrm{k} \Omega$. $17.8 \mathrm{k} \Omega$ is a standard value that is a little less than $18 \mathrm{k} \Omega$ but very close. The chart reads approximately 0.525 A as a typical $\mathrm{I}_{\text {Lim }}$ value for $17.8 \mathrm{k} \Omega$. Multiplying 0.525 A by 0.75 and 1.25 shows that the AAT4610B will limit the load current to greater than 0.394 A but less than 0.656A.

## Operation in Current Limit

When a heavy load is applied to the output of the AAT4610B, the load current is limited to the value of $\mathrm{I}_{\text {LIM }}$ determined by $\mathrm{R}_{\text {SET }}$ (see Figure 2). Since the load is demanding more current than $\mathrm{I}_{\text {LIM, }}$ the voltage at the output drops. This causes the AAT4610B to dissipate a larger than normal quantity of power, and its die temperature to increase. When the die temperature exceeds an over-temperature limit, the AAT4610B will shut down until is has cooled sufficiently, at which point it will startup again. The AAT4610B will continue to cycle on and off until the load is removed, power is removed, or until a logic high level is applied to ON.

## Enable Input

In many systems, power planes are controlled by integrated circuits which run at lower voltages than the power plane itself. The enable input ON of the AAT4610B has low and high threshold voltages that accommodate this condition. The threshold voltages are compatible with $5 \mathrm{~V} T \mathrm{~L}$ and 2.5 V to 5 V CMOS.

## Reverse Voltage

The AAT4610B is designed to control current flowing from IN to OUT. If a voltage is applied to OUT which is greater than the voltage on IN, large currents may flow. This could cause damage to the AAT4610B.

## Overload Operation



Figure 2: Overload Operation.

## Ordering Information

| Package | Enable | Marking $^{1}$ | Part Number (Tape and Reel) ${ }^{\mathbf{2}}$ |
| :---: | :---: | :---: | :---: |
| SOT23-5 | ON (active high) | 8 FXYY | AAT4610BIGV-1-T1 |
| SC70JW-8 | ON (active high) | $8 G X Y Y$ | AAT4610BIJS-1-T1 |
| SOT23-5 | ON (active low) | B9XYY | AAT4610BIGV-T1 |
| SC70JW-8 | ON (active low) | B8XYY | AAT4610BIJS-T1 |

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## Package Information

## SOT23-5



All dimensions in millimeters.

[^2]
## SC70JW-8



All dimensions in millimeters.

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[^0]:     specified is not implied. Only one Absolute Maximum Rating should be applied at any one time.
    2. Human body model is a 100 pF capacitor discharged through a $1.5 \mathrm{k} \Omega$ resistor into each pin.
    3. Mounted on a demo board.

[^1]:    1. For $\mathrm{V}_{\mathrm{IN}}$ outside this range, consult Typical $\mathrm{ON}(\overline{\mathrm{ON}})$ Threshold curve.
[^2]:    1. $X Y Y=$ assembly and date code.
    2. Sample stock is generally held on part numbers listed in BOLD.
